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Figure 2.22 Simple parallel circuit. Since all of the circuit elements are in parallel, the voltage  $v(t)$  appears across each of them. Furthermore, an examination of the circuit indicates that the current  $i_1(t)$  is into the upper node of the circuit and the currents  $i_2(t)$  and  $i_3(t)$  are out of the node.

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14 Nov, 2017 in Electricity tagged Basic Engineering Circuit Analysis - 10th Edition / BECA - Chapter 3 Assuming an ideal op-amp, determine the voltage gain of the circuit in Fig. P4.9.

Solution: Show me the final answer? [...]

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