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FPGAs can become video generators easily. The pong game consists of a ball bouncing on a screen. A paddle (controlled from a mouse here) enables the user to make the ball bounce back up. We use a Pluto FPGA board, although any other FPGA development board would work.

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Pong P. Chu, PhD, is Associate Professor in the Department of Electrical and Computer Engineering at Cleveland State University in Ohio.He has taught undergraduate- and graduate-level digital systems and computer architecture courses for more than a decade and has received instructional grants from the National Science Foundation and Cleveland State University.

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Pong P. Chu is the author of FPGA Prototyping by VHDL Examples (4.45 avg rating, 33 ratings, 5 reviews, published 2007), RTL Hardware Design Using VHDL (...

Dr. Pong P. Chu joined the Department of Electrical Engineering and Computer Science at Cleveland State University in 1989 and currently is an Associate Professor. He received the M.S. and Ph.D. degrees in Computer Engineering from Iowa State University.His research and teaching are in the area of digital systems and he is the author of seven textbooks.