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6 Chapter 7 Problem: 7.96 1. The schematic for this problem is shown below 2. The transistor used here has  $k n' = 71.2 \mu\text{A}/\text{V}^2$ . So,  $W/L = 14\mu/0.5\mu$  is chosen to get  $k n = 2 \text{ mA}/\text{V}^2$ . 3. Simulate the netlist and find out the operating voltages. 4. The other operating parameters are 5.

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*Design a circuit based on the topology of the noninverting ...*

2 Chapter 12 4. The cross over interval is  $2 \times 2.9 \mu\text{s} = 5.8 \mu\text{s}$ . So, it is 5.8 %. 5. Run the parametric analysis and sweep RL from 500? to 700? in steps of 50 ? or smaller. Plot V(VO) as shown below. 6. The output voltage is half of the input voltage when  $R_L = 650 ?$ . Netlist: [Solutions for Chapter 2: Microelectronic Circuits 6th ...](#)

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