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GATE Electrical Engineering (EE) 2020 exam date and exam schedule has been announced by IIT Delhi. The GATE exam takes place on 8th February 2020 (Saturday). Moreover, check the GATE Electrical 2020 details such as exam date, apply online, eligibility, syllabus, books, papers, notes, admit card, cut off, result and test series.

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Note: This alphabetical index to the Guide to Federal Records in the National Archives of the United States is based on a paper version with the same title compiled in 1995. The index does not reflect updates to the Guide. The updated web version of the Guide can be searched with the Search the Guide feature.

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Leading and lagging current are phenomena that

occur as a result of alternating current. In a circuit with alternating current, the value of voltage and current vary sinusoidally. In this type of circuit, the terms lead, lag, and in phase are used to describe current with reference to voltage. Current is in phase with voltage when there is no phase shift between the sinusoids describing their

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Leading and lagging current - Wikipedia

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