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Pedroni teaches synthesizable VHDL, the kind actually used by electronic design automation tools to make real circuits, and he manages to introduce the language, application, and software tool at the same time. The book is probably too basic for a practitioner wishing to brush up on the language or synthesis, but for the beginner it rocks.

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Circuit Design with VHDL: Pedroni, Volnei A.: Amazon.sg: Books circuit design and simulation with vhd 2nd edition volnei a. pedroni mit press, 2010 book web: www.vhdl.us solutions manual (v4) vhdl chapter 1:

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CIRCUIT DESIGN WITH VHDL (MIT PRESS) By Volnei A. Pedroni ...

Prof. Volnei A. Pedroni. The author received his MSc and PhD degrees in Electrical Engineering from the California Institute of Technology (Caltech), with emphasis on VLSI design, analog and digital. He is a Professor of Electronics Engineering at Brazil's UTFPR and a regular Visiting Faculty of Electrical Engineering at Caltech. He has done collaboration also with other Institutions, in USA and Italy (he is a Brazilian and Italian citizen).

Circuit Design with VHDL: Pedroni, Volnei A.: Amazon.sg: Books

The book focuses on the use of VHDL rather than solely on the language, with an emphasis on design examples and laboratory exercises. The third edition begins with a detailed review of digital circuits (combinatorial, sequential, state machines, and FPGAs), thus providing a self-contained single reference for the teaching of digital circuit design with VHDL.