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appliance, vehicle, or other device has one or more computers embedded inside it. One cannot purchase a current-model automobile, for example, without several computers on board to do everything from monitoring exhaust emissions, to operating the anti-lock brakes, to telling the transmission when

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curriculum at the junior or senior level. Students should have had a course(s) covering introductory topics in digital logic and computer organization. While this is not a text for a programming course, the reader should be familiar with computer programming concepts in at least one language such as C, C++, or

Java. Previous courses in operating systems, assembly language, and/or systems programming would be helpful, but are not essential.

Chandresh Agrawal
Exam board: SQA Level: National 5
Subject: Computing Science
First teaching: August 2017
First exam: Summer 2018

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together in one resource. Note: 1) The Reference Guide is based on the latest Scrum guides. 2) Information and Content found on the Scrum Guide, Nexus Guides and other articles (found on Scrum.org) is repeated on this Reference guide. 3) This Reference guide is not a text book or a replacement to the Scrum Guide. It's simply your workbook which has content presented systematically to understand and memorize for the exam. 4) The Reference guide also has questions and answers

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Professional Scrum Master II (Level 2) Questions and Answers.
PSM II : Quick Reference Guide and Exam Questions Kaplan AEC Architecture
This course by Lester Wertheimer, FAIA, deals with the ideas and examples which have established the architectural tradition of the Western world. Although there is no written test specifically on this subject, architectural history questions are included in many sections of the exam. The seven lessons review the theories, influences, characteristics, and major monuments from ancient times through the 20th century,

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The course is attractively illustrated with numerous sketches, plans, and diagrams, and enhanced by a glossary, bibliography, and an extensive index. A short review quiz follows each lesson, and the course concludes with a major examination. Complete explanations of all quiz and examination answers are included.

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Practice: National 5

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instruction level parallelism,
instruction set principles,
interconnection networks,
memory hierarchy design,
networks, storage and
peripherals, pipe-lining in
computer architecture, pipe-
lining performance, processor

datapath and control,
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