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Computer Organization and Design OECD Publishing

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Information Systems for Business and Beyond Exskillence

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Computer Architecture MCQs Pearson Education India

Computer Architecture MCQsMultiple Choice Questions and Answers (Quiz & Practice Tests with Answer Key) (Computer Science Quick Study Guides & Terminology Notes to Review)Bushra Arshad Grade 9 Biology Multiple Choice Questions and Answers (MCQs) Bushra Arshad Intelligent readers who want to build their own embedded computer systems-- installed in everything from cell phones to cars to handheld organizers to refrigerators-- will find this book to be the most in-depth, practical, and up-to-date guide on the market. Designing Embedded Hardware carefully steers between the practical and philosophical aspects, so developers can both create their own devices and gadgets and customize and extend off-the-shelf systems. There are hundreds of books to choose from if you need to learn programming, but only a few are available if you want to learn to create hardware. Designing Embedded Hardware provides software and hardware engineers with no prior experience in embedded systems with the necessary conceptual and design building blocks to understand the architectures of embedded systems. Written to provide the depth of coverage and real-world examples developers need, Designing Embedded Hardware also provides a road-map to the pitfalls and traps to avoid in designing embedded systems. Designing Embedded Hardware covers such essential topics as: The principles of developing computer hardware Core hardware designs Assembly language concepts Parallel I/O Analog-digital conversion Timers (internal and external) UART Serial Peripheral Interface Inter-Integrated Circuit Bus Controller Area Network (CAN) Data Converter Interface (DCI) Low-power operation This invaluable and eminently useful book gives you the practical tools and skills to develop, build, and program your own application-specific computers.

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COMPUTER ARCHITECTURE Bushra Arshad

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Bushra Arshad

MCQs (Multiple Choice Questions) in COMPUTER ARCHITECTURE is a comprehensive questions answers quiz book for undergraduate students. This quiz book comprises question on COMPUTER ARCHITECTURE practice questions, COMPUTER ARCHITECTURE test questions, fundamentals of COMPUTER ARCHITECTURE practice questions, COMPUTER ARCHITECTURE questions for competitive examinations and practice questions for COMPUTER ARCHITECTURE certification. In addition, the book consists of Sufficient number of COMPUTER ARCHITECTURE MCQ (multiple choice questions) to understand the concepts better. This book is essential for students preparing for various competitive examinations all over the world. Increase your understanding of COMPUTER ARCHITECTURE Concepts by using simple multiple-choice questions that build on each other. Enhance your time-efficiency by reading these on your smartphone or tablet during those down moments between classes or errands. Make this a game by using the study sets to quiz yourself or a friend and reward yourself as you improve your knowledge.

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This is a quick assessment book / quiz book. It has a wide variety of over 1,600 questions, with answers on Operating Systems. The questions have a wide range of difficulty levels and are designed to test a thorough understanding of the topical material. The book covers questions on the operating systems structures, fundamentals of processes and threads, CPU scheduling, process synchronization, deadlocks, memory management, I/O subsystem, and mass storage (disk) structures.

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"Information Systems for Business and Beyond introduces the concept of information systems, their use in business, and the larger impact they are having on our world."--BC Campus website.

Designing Embedded Hardware Bushra Arshad

This book is a comprehensive text on basic, undergraduate-level computer architecture. It starts from theoretical preliminaries and simple Boolean algebra. After a quick discussion on logic gates, it describes three classes of assembly languages: a custom RISC ISA called SimpleRisc, ARM, and x86. In the next part, a processor is designed for the SimpleRisc ISA from scratch. This includes the combinational units, ALUs, processor, basic 5-stage pipeline, and a microcode-based design. The last part of the book discusses caches, virtual memory, parallel programming, multiprocessors, storage devices and modern I/O systems. The book's website has links to slides for each chapter and video lectures hosted on YouTube.