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Question 1. In a k-

way set associative cache, the cache is divided into v sets, each of which consists of k lines. The lines of a set are placed in sequence one after another. The lines in set s are sequenced before the lines in set $(s+1)$. The main memory blocks are numbered 0 onwards. Computer Organization And Architecture Problems Our 1000+ Computer Organization & Architecture questions and

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in both situations. In order to find out N it is necessary to write the CPU_time for both ...

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3. Data Transfer Rate-. The amount of data that passes under the read / write head in a given amount of time is called as data transfer rate. The time taken to transfer the data is called as transfer time. It depends on the following factors-. Number of bytes to be transferred. Rotation speed of the disk.
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processed, and the results go out immediately).

Processing uses the processor or random access memory, otherwise known as RAM.

Processing involves the
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