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The Conformal LEC software infers a latch primitive in the Conformal LEC software library to implement a latch.

This results in having a library on the golden side and a combinational loop with a cut point on the revised side, leading to verification mismatches.

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Cadence Conformal suite of tools contains a tool called Logic Equivalence Checker or LEC. Section 4 of this tutorial describes how to formally verify that the synthesized design is functionally equivalent to the RTL description using LEC. This section can be skipped if one chooses to use Formality for equivalence checking.

[Equivalence Checking Using Cadence Conformal LEC](#)

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Cadence Conformal LEC Formal Hardware Verification (COEN 7501) Summer 2010. The Golden and the Revised Designs Revised Design Golden Design Golden Revised Conformal LEC: =? NO YES. 1. In the SETUP mode, Load the Golden and Revised Designs 2. Change System Mode to LEC 3. Add all ports as compare points 4. Compare. Report AEMapped Points Diagnose. LEC Diagnosis Manager Lists the failed compare ...

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LEC. This section can be skipped if one chooses to
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Manuals for historic LEC products are listed below.
You can search for a manual using the model
description. The model description can be found on
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Conformal Smart LEC - Cadence Design Systems

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