

# Cortex M3 User Manual

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[UM10430 LPC18xx ARM Cortex-M3 microcontroller](#)

ETM-M3 Technical Reference Manual The ETM-M3 TRM describes the functionality and behavior of the Cortex M3 Embedded Trace Macrocell. It is required at all stages of the design flow. Typically the ETM-M3 is integrated with the Cortex M3 processor prior to implementation as a single macrocell.

[Linux Cortex-M User's Manual - Microsemi](#)

Cortex-M3 Instruction Set. Table 1-1. Cortex-M3 Instructions (continued)

Mnemonic Operands Brief Description Flags See Page ORR, ORRS {Rd,} Rn, Op2

Logical OR N,Z,C 43 POP reglist Pop registers from stack - 37 PUSH reglist

Push registers onto stack - 37 RBIT Rd, Rn Reverse bits - 55

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ARM's developer website includes documentation, tutorials, support resources and more. Over the next few months we will be adding more developer resources and documentation for all the products and technologies that ARM provides.

[Cortex-M3 Reference Manual - silabs.com](#)

For information on the ARM Cortex-M3 core, refer to the Cortex-M3 Technical Reference Manual. Related documents Available from [www.arm.com](#): • Cortex-M3 Technical Reference Manual Available from [www.st.com](#): • STM32F100xx datasheets • STM32F100xx Flash programming manual [www.st.com](#)

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Cortex-M3 – Arm

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[Cortex-M3 Technical Reference Manual - Keil](#)

This manual is written to help system designers, system integrators, verification engineers, and software programmers who are implementing a System-on-Chip (SoC) device based on the Cortex M3 processor.

[RM0041 Reference manual - STMicroelectronics](#)

Table 2-4 lists the 16-bit Cortex-M3 instructions. Table 2-4 16-bit Cortex-M3 instruction summary Operation

Assembler Add register value and C flag to register value ADC <Rd>, <Rm> Add immediate 3-bit value to register ADD <Rd>, <Rn>, #<immed\_3>

[ARM Cortex-M - Wikipedia](#)

Cortex-M3 programming manual Introduction This programming manual provides information for application and system-level software developers. It gives a full description of the STM32F10xxx/20xxx/21xxx/L1xxxx Cortex M3 processor programming model, instruction set and core peripherals.

[Cortex-M3/M4F Instruction Set Technical User's Manual \(Rev. A\)](#)

Programmer's Model Instruction set The Cortex-M3 processor does not support ARM instructions. The Cortex-M3 processor supports all ARMv6 Thumb instructions except those listed in Table 2-4. Table 2-4 Nonsupported Thumb instructions Instruction Action if executed BLX(1) Branch with link and exchange BLX(1) always faults.

[ARM CORTEX-M3 TECHNICAL REFERENCE MANUAL Pdf Download.](#)

The ARM Cortex-M is a group of 32-bit RISC ARM processor cores licensed by Arm Holdings. They are intended for microcontroller use, and have been shipped in tens of billions of devices. The cores consist of the Cortex-M0, Cortex-M0+, Cortex-M1, Cortex-M3, Cortex-M4, Cortex-M7, Cortex-M23, Cortex-M33, Cortex-M35P.

[Cortex-M3 Devices Generic User Guide – Arm Developer](#)

Linux Cortex-M User's Manual 3/21 Release 1.10.1. Overview This document is a User's Manual for Linux Cortex-M covering the following products: Linux STM32, supporting the STMicroelectronics Cortex-M3 based STM32F2 and Cortex-M4 based STM32F4 microcontrollers; Linux LPC, supporting the NXP Cortex-M3 based LPC178X, LPX18XX and LPC43XX

1.2 About the EFM32 Cortex-M3 processor and core peripherals The EFM32 Cortex™ -M3 processor is a high performance 32-bit processor designed for the microcontroller market. It offers significant benefits to developers, including: • outstanding processing performance combined with fast interrupt handling

[UM10470 LPC178x/7x User manual - NXP Semiconductors](#)

User manual Rev. 4.1 — 19 December 2016 5 of 851 1.1 Introduction The LPC176x/5x is an ARM Cortex-M3 based microcontroller for embedded applications requiring a high level of integration and low power dissipation.

[Cortex-M3 Instruction Set Technical User's Manual](#)

Linux Cortex-M User's Manual 3/21 Release 1.9.0.1. Overview This document is a User's Manual for Linux Cortex-M covering the following products: • Linux STM32, supporting the STMicroelectronics Cortex-M3 based STM32F2 and Cortex-M4 based STM32F4 microcontrollers; • Linux LPC, supporting the NXP Cortex-M3 based LPC178X, LPX18XX and LPC43XX

[ARM Cortex M3 Processor Technical Reference Manual ...](#)

User manual Rev. 2.2 — 25 January 2013 6 of 1254 1.1 Introduction The LPC18xx are ARM Cortex-M3 based microcontrollers for embedded applications. The ARM Cortex-M3 is a next generation core that offers system enhancements such as low power consumption, enhanced debug features, and a high level of support block integration.

[PM0056 Programming manual - STMicroelectronics](#)

The Cortex-M processor series is designed to enable developers to create cost-sensitive and power-constrained solutions for a broad range of devices. The optimal balance between area, performance, and power makes Cortex-M3 ideal for products such as microcontrollers, automotive body systems, and wireless networking and sensors.

[Cortex M3 User Manual](#)

User manual Rev. 4.0 — 19 December 2016 4 of 1110 1.1 Introduction The LPC178x/177x is an ARM Cortex-M3 based microcontroller for embedded applications requiring a high level of integration and low power dissipation.

[UM10360 LPC176x/5x User manual - NXP Semiconductors](#)

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