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Page 1 of 329. CORRECTIONS TO SOLUTIONS MANUAL. In the new edition, some chapter problems have been reordered and equations and figure refer- ences have changed.

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SM 1 SM 1 EECE 488 — Set 1: Introduction and Background. EECE488: Analog CMOS Integrated Circuit Design Introduction and Background. Shahriar Mirabbasi Department of Electrical and Computer Engineering University of British Columbia shahriar@ece.ubc.ca. Technical contributions of Pedram Lajevardi in revising the slides is greatly acknowledged.

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