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Q. 5.6: A sequential circuit with two D flip-flops A and B, two inputs, x and y; and one output z is
Q. 5.1: The D latch of Fig. 5.6 is constructed with four NAND gates and an inverter. Consider the
Q. 4.8: Design a code converter that converts a decimal digit from the 8, 4, -2, -1 code to BCD
Q. 5.4: A PN flip-flop has four operations: clear to 0, no change, complement, and set to 1, when
Q. 5.8: Derive the state table and the state diagram of the sequential circuit shown in Fig. P5.8

Q. 4.26: Construct a 4-to-16-line decoder with five 2-to-4-line decoders with enable.

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Q. 1.1: List the octal and

hexadecimal numbers from 16 to 32. Using A and B for the last two digits
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~~M. Morris Mano is an Emeritus Professor of Computer Engineering at the California State University, Los Angeles. His notable works include the Mano Machine, i.e. a theoretical computer that contains a central processing unit, random access memory, and an input-output bus. M. Morris Mano has authored numerous books in the area of digital circuits that are known for teaching the basic concepts ...~~

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