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*XIV Mediterranean Conference on Medical and Biological Engineering and Computing 2016* Springer Science & Business Media

This book provides design methods for Digital Signal Processors and Application Specific Instruction set Processors, based on the author's extensive, industrial design experience. Top-down and bottom-up design methodologies are presented, providing valuable guidance for both students and practicing design engineers. Coverage includes design of internal-external data types, application specific instruction sets, micro architectures, including designs for datapath and control path, as well as memory sub systems. Integration and verification of a DSP-ASIP processor are discussed and reinforced with extensive examples. Instruction set design for application specific processors based on fast application profiling Micro architecture design methodology Micro architecture design details based on real examples Extendable architecture design protocols Design for efficient memory sub systems (minimizing on chip memory and cost) Real example designs based on extensive, industrial experiences

**Hardware/Software Co-Design** Goodheart-Wilcox Publisher

This title builds on the student's background from a first course in logic design and focuses on developing, verifying, and synthesizing designs of digital circuits. The Verilog language is introduced in an integrated, but selective manner, only as needed to support design examples.

*Principles of Digital Information Technology* Springer Science & Business Media

The skills and guidance needed to master RTL hardware design This book teaches readers how to systematically design efficient, portable, and scalable Register Transfer Level (RTL) digital circuits using the VHDL hardware description language and synthesis software. Focusing on the module-level design, which is composed of functional units, routing circuit, and storage, the book illustrates the relationship between the VHDL constructs and the underlying hardware components, and shows how to develop codes that faithfully reflect the module-level design and can be synthesized into efficient gate-level implementation. Several unique features distinguish the book: \* Coding style that shows a clear relationship between VHDL constructs and hardware components \* Conceptual diagrams that illustrate the realization of VHDL codes \* Emphasis on the code reuse \* Practical examples that demonstrate and reinforce design concepts, procedures, and techniques \* Two chapters on realizing sequential algorithms in hardware \* Two chapters on scalable and parameterized designs and coding \* One chapter covering the synchronization and interface between multiple clock domains Although the focus of the book is RTL synthesis, it also examines the synthesis task from the perspective of the overall development process.

Readers learn good design practices and guidelines to ensure that an RTL design can accommodate future simulation, verification, and testing needs, and can be easily incorporated into a larger system or reused. Discussion is independent of technology and can be applied to both ASIC and FPGA devices. With a balanced presentation of fundamentals and practical examples, this is an excellent textbook for upper-level undergraduate or graduate courses in advanced digital logic. Engineers who need to make effective use of today's synthesis software and FPGA devices should also refer to this book.

**Verilog for Digital Design** John Wiley & Sons Construction Delays, Third Edition, provides the latest specialized tools and techniques needed to avoid delays on construction projects. These include institutional, industrial, commercial, hi-rise, power and water, transportation and marine construction projects. Most other references provide only post facto construction delay analysis. This update includes 18 chapters, 105 sections and approximately 100 new pages relative to the second edition. Features greatly expanded discussion of the project management concerns related to construction delays, including a more comprehensive discussion of the development and review of the project

schedule Offers a detailed analysis of the strengths and weaknesses of the most common construction delay approaches and how they should be properly deployed or avoided Includes significant discussion of the contract provisions governing scheduling, the measurement of delays and payments for delay Includes numerous real world case studies

Digital Design John Wiley & Sons

This is a practical book for computer engineers who want to understand or implement hardware/software systems. It focuses on problems that require one to combine hardware design with software design — such problems can be solved with hardware/software codesign. When used properly, hardware/software codesign works better than hardware design or software design alone: it can improve the overall performance of digital systems, and it can shorten their design time. Hardware/software codesign can help a designer to make trade-offs between the flexibility and the performance of a digital system. To achieve this, a designer needs to combine two radically different ways of design: the sequential way of decomposition in time, using software, with the parallel way of decomposition in space, using hardware. Intended Audience This book assumes that you have a basic understanding of hardware that you are familiar with standard digital hardware components such as registers, logic gates, and components such as multiplexers and arithmetic operators. The book also assumes that you know how to write a program in C. These topics are usually covered in an introductory course on computer engineering or in a combination of courses on digital design and software engineering.

**Digital System Test and Testable Design** Elsevier

\* Ideal as either a standalone introductory guide or in tandem with Vahid's Digital Design to allow for greater language coverage, this is an accessible introductory guide to hardware description language \* VHDL is a hardware description language used to model electronic systems and this book is helpful for anyone who is starting out and learning the language \* Features numerous examples and tips in the margins \* Focuses on application and use of the language, rather than just teaching the basics of the language

**Real-Time Systems Design and Analysis** Packt Publishing Ltd A practical guide to the methodologies used in language teaching and learning research, providing expert advice and real-life examples from leading TESOL researchers Research Methods in Language Teaching and Learning provides practical guidance on the primary research methods used in second language teaching, learning, and education. Designed to support researchers and students in language education and learning, this highly accessible book covers a wide range of research methodologies in the context of actual practice to help readers fully understand the process of conducting research. Organized into three parts, the book covers qualitative studies, quantitative studies, and systematic reviews. Contributions by an international team of distinguished researchers and practitioners explain and demonstrate narrative inquiry, discourse analysis, ethnography, heuristic inquiry, mixed methods, experimental and quasi-experimental studies, and more. Each chapter presents an overview of a method of research, an in-depth description of the research framework or data analysis process, and a meta-analysis of choices made and challenges encountered. Offering invaluable insights and hands-on research knowledge to students and early-career practitioners alike, this book: Focuses on the research methods, techniques, tools, and practical aspects of performing research Provides firsthand narratives and case studies to explain the decisions researchers make Compares the relative strengths and weaknesses of different research methods Includes real-world examples for each research method and framework to highlight the context of the study Includes extensive references, further reading suggestions, and end-of-chapter review questions Part of the Guides to Research Methods in Language and Linguistics series, Research Methods in Language Teaching and Learning is essential reading for students, educators, and researchers in all related fields, including TESOL, second language acquisition, English language teaching, and applied linguistics.

**Internet of Things IGI Global**

An introduction to the engineering principles of embedded systems, with a focus on modeling, design, and analysis of cyber-physical systems. The most visible use of computers and software is processing information for human consumption. The vast majority of computers in use, however, are much less visible. They run the engine, brakes, seatbelts, airbag, and audio system in your car. They digitally encode your voice and construct a radio signal to send it from your cell phone to a base station. They command robots on a factory floor, power generation in a power plant, processes in a chemical plant, and traffic lights in a city. These less visible computers are called embedded systems, and the software they run is called embedded software. The principal challenges in designing and analyzing embedded systems stem from their interaction with physical processes. This book takes a cyber-physical approach to

embedded systems, introducing the engineering concepts underlying embedded systems as a technology and as a subject of study. The focus is on modeling, design, and analysis of cyber-physical systems, which integrate computation, networking, and physical processes. The second edition offers two new chapters, several new exercises, and other improvements. The book can be used as a textbook at the advanced undergraduate or introductory graduate level and as a professional reference for practicing engineers and computer scientists. Readers should have some familiarity with machine structures, computer programming, basic discrete mathematics and algorithms, and signals and systems.

**Verilog for Digital Design** Set Elsevier

\* Ideal as either a standalone introductory guide or in tandem with Vahid's Digital Design to allow for greater language coverage, this is an accessible introductory guide to hardware description language \* Verilog is a hardware description language used to model electronic systems (sometimes called Verilog HDL) and this book is helpful for anyone who is starting out and learning the language \* Focuses on application and use of the language, rather than just teaching the basics of the language Digital System Design with FPGA: Implementation Using Verilog and VHDL Morgan Kaufmann

An eagerly anticipated, up-to-date guide to essential digital design fundamentals Offering a modern, updated approach to digital design, this much-needed book reviews basic design fundamentals before diving into specific details of design optimization. You begin with an examination of the low-levels of design, noting a clear distinction between design and gate-level minimization. The author then progresses to the key uses of digital design today, and how it is used to build high-performance alternatives to software. Offers a fresh, up-to-date approach to digital design, whereas most literature available is sorely outdated Progresses through low levels of design, making a clear distinction between design and gate-level minimization Addresses the various uses of digital design today Enables you to gain a clearer understanding of applying digital design to your life With this book by your side, you'll gain a better understanding of how to apply the material in the book to real-world scenarios.

**Combinational Logic Design** Springer

This book is about digital system testing and testable design. The concepts of testing and testability are treated together with digital design practices and methodologies. The book uses Verilog models and testbenches for implementing and explaining fault simulation and test generation algorithms. Extensive use of Verilog and Verilog PLI for test applications is what distinguishes this book from other test and testability books. Verilog eliminates ambiguities in test algorithms and BIST and DFT hardware architectures, and it clearly describes the architecture of the testability hardware and its test sessions. Describing many of the on-chip decompression algorithms in Verilog helps to evaluate these algorithms in terms of hardware overhead and timing, and thus feasibility of using them for System-on-Chip designs. Extensive use of testbenches and testbench development techniques is another unique feature of this book. Using PLI in developing testbenches and virtual testers provides a powerful programming tool, interfaced with hardware described in Verilog. This mixed hardware/software environment facilitates description of complex test programs and test strategies.

**Algorithms** Cengage Learning

This book is designed to serve as a hands-on professional reference with additional utility as a textbook for upper undergraduate and some graduate courses in digital logic design. This book is organized in such a way that that it can describe a number of RTL design scenarios, from simple to complex. The book constructs the logic design story from the fundamentals of logic design to advanced RTL design concepts. Keeping in view the importance of miniaturization today, the book gives practical information on the issues with ASIC RTL design and how to overcome these concerns. It clearly explains how to write an efficient RTL code and how to improve design performance. The book also describes advanced RTL design concepts such as low-power design, multiple clock-domain design, and SOC-based design. The practical orientation of the book makes it ideal for training programs for practicing design engineers and for short-term vocational programs. The contents of the book will also make it a useful read for students and hobbyists.

**Digital Signal Processing with Field Programmable Gate Arrays** Wiley

Digital Design with RTL Design, VHDL, and Verilog John Wiley & Sons

Out-of-order Parallel Discrete Event Simulation for Electronic System-level Design Springer

Digital Design provides a modern approach to learning the increasingly important topic of digital systems design. The text's focus on register-transfer-level design and present-day applications not only leads to a better appreciation of computers and of today's ubiquitous digital devices, but also provides for a better understanding of careers involving digital design and embedded system design.

1. Introduction
2. Combinational Logic Design
3. Sequential Logic Design-Controllers
4. Datapath Components
5. Register-Transfer Level (RTL) Design
6. Optimizations and Tradeoffs
7. Physical Implementation
8. Programmable Processors
9. Hardware Description Languages

Advanced Digital Design with the Verilog HDL John Wiley & Sons

This book provides an approach toward the applications and principle theory of digital signal processing in modern intelligent systems, biological engineering, telecommunication, and information technology. Assuming the reader already has prior knowledge of signal processing theory, this book will be useful for finding novel methods that fit special needs in digital signal processing (DSP). The combination of signal processing and intelligent systems in hybrid structures rather than serial or parallel processing provide the best mechanism that is a better fit with the comprehensive nature of human. This book is a practical reference that places the emphasis on principles and applications of DSP in digital systems. It covers a broad area of digital systems and applications of machine learning methods including convolutional neural networks, evolutionary algorithms, adaptive filters, spectral estimation, data compression and functional verification. The level of the book is ideal for professional DSP users and useful for graduate students who are looking for solutions to their design problems. The theoretical principles provide the required base for comprehension of the methods and application of modifications for the special needs of practical projects.

Research Methods in Language Teaching and Learning John Wiley & Sons Incorporated

This book introduces a modern approach to embedded system design, presenting software design and hardware design in a unified manner. It covers trends and challenges, introduces the design and use of single-purpose processors ("hardware") and general-purpose processors ("software"), describes memories and buses, illustrates hardware/software tradeoffs using a digital camera example, and discusses advanced computation models, controls systems, chip technologies, and modern design tools. For courses found in EE, CS and other engineering departments.

Specification and Design of Embedded Systems Pearson Education India

An important guide that reviews the basics of magnetic biosensor modeling and simulation Magnetic Sensors for Biomedical Applications offers a comprehensive review of magnetic biosensor modelling and simulation. The authors—noted experts on the topic—explore the model's strengths and weaknesses and discuss the competencies of different modelling software, including homemade and commercial (for example Multi-physics modelling software). The section on sensor materials examines promising materials whose properties have been used for sensing action and predicts future smart-materials that have the potential for sensing application. Next, the authors present classifications of sensors that are divided into different sub-types. They describe their working and highlight important applications that reveal the benefits and drawbacks of relevant designs. The book also contains information on the most recent developments in the field of each sensor type. This important book: Provides an even treatment of the major foundations of magnetic biosensors Presents problem solution methods such as analytical and numerical Explains how solution methods complement each other, and offers information on their materials, design, computer aided modelling and simulation, optimization, and device fabrication Describes modeling work challenges and solutions Written for students in electrical and electronics engineering, physics, chemistry, biomedical engineering, and biology, Magnetic Sensors for Biomedical Applications offers a guide to the principles of biomagnetic sensors, recent developments, and reveals the impact of sensor modelling and simulation on magnetic sensors.

RTL Hardware Design Using VHDL Morgan Kaufmann

The book is divided into four major parts. Part I covers HDL constructs and synthesis of basic digital circuits. Part II provides an overview of embedded software development with the emphasis on low-level I/O access and drivers. Part III demonstrates the design and development of hardware and software for several complex I/O peripherals, including PS2 keyboard and mouse, a graphic video controller, an audio codec, and an SD (secure digital) card. Part IV provides three case studies of the integration of hardware accelerators, including a custom GCD (greatest common divisor) circuit, a Mandelbrot set fractal circuit, and an audio synthesizer based on DDFS (direct digital frequency synthesis) methodology. The book utilizes FPGA devices, Nios II soft-core processor, and development platform from Altera Co., which is one of the two main FPGA manufactures. Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions (details at <http://www.altera.com/university>). The two main educational prototyping boards are known as DE1 (\$99) and DE2 (\$269). All experiments can be implemented and tested with these boards. A board combined with this book becomes a "turn-key" solution for the SoPC design experiments and projects. Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I/O configuration.

Handbook of Industrial Inkjet Printing Springer Science & Business Media

DIGITAL SYSTEMS DESIGN USING VERILOG integrates coverage of logic design principles, Verilog as a hardware design language, and FPGA implementation to help electrical and computer engineering students master the process of designing and testing new hardware configurations. A Verilog equivalent of authors Roth and John's previous successful text using VHDL, this practical book presents Verilog constructs side-by-side with hardware, encouraging students to think in terms of desired hardware while writing synthesizable Verilog. Following a review of the basic concepts of logic design, the authors introduce the basics of Verilog using simple combinational circuit examples, followed by models for simple sequential circuits. Subsequent chapters ask readers to tackle more and more complex designs. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

Magnetic Sensors for Biomedical Applications John Wiley & Sons Until the late 1980s, information processing was associated with large mainframe computers and huge tape drives. During the 1990s, this trend shifted toward information processing with personal computers, or PCs. The trend toward miniaturization continues and in the future the majority of information processing systems will be small mobile computers, many of which will be embedded into larger products and interfaced to the physical environment. Hence, these kinds of systems are called embedded systems. Embedded systems together with their physical environment are called cyber-physical systems. Examples include systems such as transportation and fabrication equipment. It is expected that the total market volume of embedded systems will be significantly larger than that of traditional information processing systems such as PCs and mainframes. Embedded systems share a number of common characteristics. For example, they must be dependable, efficient, meet real-time constraints and require customized user interfaces (instead of generic keyboard and mouse interfaces). Therefore, it makes sense to consider common principles of embedded system design. Embedded System Design starts with an introduction into the area and a survey of specification models and languages for embedded and cyber-physical systems. It provides a brief overview of hardware devices used for such systems and presents the essentials of system software for embedded systems, like real-time operating systems. The book also discusses evaluation and validation techniques for embedded systems. Furthermore, the book presents an overview of techniques for mapping applications to execution platforms. Due to the importance of resource efficiency, the book also contains a selected set of optimization techniques for embedded systems, including special compilation techniques. The book closes with a brief survey on testing. Embedded System Design can be used as a text book for courses on embedded systems and as a source which provides pointers to relevant material in the area for PhD students and teachers. It assumes a basic knowledge of information processing hardware and software. Courseware related to this book is available at <http://ls12-www.cs.tu-dortmund.de/~marwedel>.