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Techniques for low emission and susceptibility John Wiley & Sons
Elektromagnetische Verträglichkeit12. Internationale Fachmesse und Kongress für Elektromagnetische Verträglichkeit ; 10. - 12. Februar 2004 Messe DüsseldorfMargret SchneiderIntelligent Systems and ApplicationsProceedings of the International Computer Symposium (ICS) Held at Taichung, Taiwan, December 12 - 14, 2014IOS Press

Next-Generation ADCs, High-Performance Power Management, and Technology Considerations for Advanced Integrated Circuits Margret Schneider

This book introduces unmanned aircraft systems traffic management (UTM) and how this new paradigm in traffic management integrates unmanned aircraft operations into national airspace systems. Exploring how UTM is expected to operate, including possible architectures for UTM implementations, and UTM services, including flight planning, strategic coordination, and conformance monitoring, Unmanned Aircraft Systems Traffic Management: UTM considers the boundaries of UTM and how it is expected to interlace with tactical coordination systems to maintain airspace safety. The book also presents the work of the global ecosystem of players advancing UTM, including relevant standards development organizations (SDOs), and considers UTM governance paradigms and challenges. FEATURES Describes UTM concept of operations (ConOps) and global variations in architectures Explores envisioned UTM services, including flight planning, strategic coordination, conformance monitoring, contingency management, constraints and geo-awareness, and remote identification Highlights cybersecurity standards development and awareness Covers approaches to the approval, management, and oversight of UTM components and ecosystem Considers the future of UTM and potential barriers to its success, international coordination, and regulatory reform This book is an essential, in-depth, annotated resource for developers, unmanned aircraft system operators, pilots, policy makers, researchers, and academics engaged in unmanned systems, transportation management, and the future of aviation.

Advances in Analog Circuit Design 2016 IET

"The increasing demand for high-speed transport of data has revitalized optical communications, leading to extensive work on high-speed device and circuit design. This book deals with the design of high-speed integrated circuits for optical communicationtransceivers.Building upon a detailed understanding of optical devices, the book describes the analysis and design of critical building blocks, such as transimpedance and limiting amplifiers, laser drivers, phase-locked loops, oscillators, clock and datarecovery circuits, and multiplexers.This second edition of this best selling textbook has been updated to provide information on the latest developments in the field"--

Advances in Analog Circuit Design 2019 Springer Nature

An Overview of a Rapidly Expanding Area in Chemistry Exploring the future in chemical analysis research, Ionic Liquids in Chemical Analysis focuses on materials that promise entirely new ways to perform solution chemistry. It provides a broad overview of the applications of ionic liquids in various areas of analytical chemistry, in

From Concept to Implementation Artech House

This comprehensive handbook provides readers with a single-source reference to the theoretical fundamentals, physical mechanisms and principles of operation of all known microwave devices and various radars. The author discusses proven methods of computation and design development, process, schematic, schematic-technical and construction peculiarities of each breed of the microwave devices, as well as the most popular and original technical solutions for radars. Coverage also includes the history of creation of the most widely used radars, as well as guidelines for their potential upgrading. Offers readers a comprehensive, systematized view of all contemporary knowledge, acquired during the last 20 years, on radars and related disciplines; Provides a single-source reference on the physical mechanisms and principles of operation of the basic components of radio location devices, including theoretical aspects of designing the necessary, high-efficiency electronic devices and systems, as well as key, practical methods of computation and design; Presents complex topics using simple language, minimizing mathematics.

A Handbook of Interconnect Theory and Design Practices Springer

This book discusses the new roles that the VLSI (very-large-scale integration of semiconductor circuits) is taking for the safe, secure, and dependable design and operation of electronic systems. The book consists of three parts. Part I, as a general introduction to this vital topic, describes how electronic systems are designed and tested with particular emphasis on dependability engineering, where the simultaneous assessment of the detrimental outcome of failures and cost of their containment is made. This section also describes the related research project "Dependable VLSI Systems," in which the editor and authors of the book were involved for 8 years. Part II addresses various threats to the dependability of VLSIs as key systems components, including time-dependent degradations, variations in device characteristics, ionizing radiation, electromagnetic interference, design errors, and tampering, with discussion of technologies to counter those threats. Part III elaborates on the design and test technologies for dependability in such applications as control of robots and vehicles, data processing, and storage in a cloud environment and heterogeneous wireless telecommunications. This book is intended to be used as a reference for engineers who work on the design and testing of VLSI systems with particular attention to dependability. It can be used as a textbook in graduate courses as well. Readers interested in dependable systems from social and industrial– economic perspectives will also benefit from the discussions in this book.

PCB Design and Layout Fundamentals for EMC IET

A cutting-edge guide to the theory and practice of high-speed digital system design An understanding of high-speed interconnect phenomena is essential for digital designers who must deal with the challenges posed by the ever-increasing operating speeds of today's microprocessors. This book provides a much-needed, practical guide to the state of the art of modern digital system design, combining easily accessible explanations with immensely useful problem-solving strategies. Written by three leading Intel engineers, High-Speed Digital System Design clarifies difficult and often neglected topics involving the effects of high frequencies on digital buses and presents a variety of proven techniques and application examples. Extensive appendices, formulas, modeling techniques as well as hundreds of figures are also provided. Coverage includes: * A thorough introduction to the digital aspects of basic transmission line theory * Crosstalk and nonideal transmission line effects on signal quality and timings * The impact of packages, vias, and connectors on signal integrity * The effects of nonideal return current paths, high frequency power delivery, and simultaneous switching noise * Explanations of how driving circuit characteristics affect the quality of the digital signal * Digital timing analysis at the system level that incorporates high-speed signaling effects into timing budgets * Methodologies for designing high-speed buses and handling the very large number of variables that affect interconnect performance * Radiated emission problems and how to minimize system noise * The practical aspects of making measurements in high-speed digital systems

Handbook of Microwave and Radar Engineering Wiley-IEEE Press

Analog Circuit Design contains the contribution of 18 tutorials of the 17th workshop on Advances in Analog Circuit Design. Each part discusses a specific to-date topic on new and valuable design ideas in the area of analog circuit design. Each part is presented by six experts in that field and state of the art information is shared and overviewed. This book is number 17 in this successful series of Analog Circuit Design.

Ultra-Wideband, Short-Pulse Electromagnetics 7 IOS Press

This book is based on the 18 tutorials presented during the 28th workshop on Advances in Analog Circuit Design. Expert designers present readers with information about a variety of topics at the frontier of analog circuit design, including next-generation analog-to-digital converters, high-performance power management systems and technology considerations for advanced IC design. For anyone involved in analog circuit research and development, this book will be a valuable summary of the state-of-the-art in these areas. Provides a summary of the state-of-the-art in analog circuit design, written by experts from industry and academia; Presents material in a tutorial-based format; Includes coverage of next-generation analog-to-digital converters, high-performance power management systems, and technology considerations for advanced IC design.

Key EMC Facts, Equations and Data Springer Nature

This much-anticipated volume builds on the author's best selling and classic work, RF Power Amplifiers for Wireless Communications (Artech House, 1999), offering experienced engineers a more in-depth understanding of the theory and design of RF power amplifiers. An invaluable reference tool for RF, digital and system level designers, the book includes discussions on the most critical topics for professionals in the field, including envelope power management schemes and linearization.

Advances in Battery Technologies for Electric Vehicles Springer

Environmental electromagnetic pollution has drastically increased over the last decades. The omnipresence of communication systems, various electronic appliances and the use of ever increasing frequencies, all contribute to a noisy electromagnetic environment which acts detrimentally on sensitive electronic equipment. Integrated circuits must be able to operate satisfactorily while cohabiting harmoniously in the same appliance, and not generate intolerable levels of electromagnetic emission, while maintaining a sound immunity to potential electromagnetic disturbances: analog integrated circuits are in particular more easily disturbed than their digital counterparts, since they don't have the benefit of dealing with predefined levels ensuring an innate immunity to disturbances. The objective of the research domain presented in EMC of Analog Integrated Circuits is to improve the electromagnetic immunity of considered analog integrated circuits, so that they start to fail at relevantly higher conduction levels than before.

High-speed Clock and Data Recovery, High-performance Amplifiers, Power Management CRC Press

This hands-on trouble-shooting style book offers step-by-step 'recipes' to assist those who are trying to solve EMI problems, by detailing exactly what to do and how to do it.

Art, Science and Experience DIANE Publishing

The Circuit Designer's Companion covers the theoretical aspects and practices in analogue and digital circuit design. Electronic circuit design involves designing a circuit that will fulfill its specified function and designing the same circuit so that every production model of it will fulfill its specified function, and no other undesired and unspecified function. This book is composed of nine chapters and starts with a review of the concept of grounding, wiring, and printed circuits. The subsequent chapters deal with the passive and active components of circuitry design. These topics are followed by discussions of the principles of other design components, including linear integrated circuits, digital circuits, and power supplies. The remaining chapters consider the vital role of electromagnetic compatibility in circuit design. These chapters also look into safety, design of production, testability, reliability, and thermal management of the designed circuit. This book is of great value to electrical and design engineers.

Sensors, Actuators and Power Drivers; Integrated Power Amplifiers from Wireline to RF; Very High Frequency Front-Ends John Wiley & Sons

This book describes for readers the entire, interconnected complex of theoretical and practical aspects of designing and organizing the production of various electronic devices, the general and main distinguishing feature of which is the high speed of processing and transmitting of digital signals. The authors discuss all the main stages of design - from the upper system level of the hierarchy (telecommunications system, 5G mobile communications) to the lower level of basic semiconductor elements, printed circuit boards. Since the developers of these devices in practice deal with distorted digital signals that are transmitted against a background of interference, the authors not only explain the physical nature of such effects, but also offer specific solutions as to how to avoid such parasitic effects, even at the design stage of high-speed devices.

Grounds for Grounding Woodhead Publishing

This book presents select proceedings of the Electric Power and Renewable Energy Conference 2020 (EPREC 2020). This book provides rigorous

discussions, case studies, and recent developments in emerging areas of control systems, especially, load frequency control, wide-area monitoring, control & instrumentation, optimization, intelligent control, energy management system, SCADA systems, etc. The contents of this book will be useful to researchers and professionals interested in control theory and its applications to power grids and systems. The book can also be used by policy makers and power engineers involved in power generation and distribution.

Designing Electronic Systems for EMC CRC Press

This book addresses the various challenges and open questions relating to CAN communication networks. Opening with a short introduction into the fundamentals of CAN, the book then examines the problems and solutions for the physical layout of networks, including EMC issues and topology layout. Additionally, a discussion of quality issues with a particular focus on test techniques is presented. Each chapter features a collection of illuminating insights and detailed technical information supplied by a selection of internationally-regarded experts from industry and academia. Features: presents thorough coverage of architectures, implementations and application of CAN transceiver, data link layer and so-called higher layer software; explains CAN EMC characteristics and countermeasures, as well as how to design CAN networks; demonstrates how to practically apply and test CAN systems; includes examples of real networks from diverse applications in automotive engineering, avionics, and home heating technology.

Machine Learning Applications in Electromagnetics and Antenna Array Processing Artech House

A Landmark text thoroughly updated, including a new CD As digital devices continue to be produced at increasingly lowercosts and with higher speeds, the need for effective electromagnetic compatibility (EMC) design practices has become more critical than ever to avoid unnecessary costs in bringing products into compliance with governmental regulations. The Second Edition of this landmark text has been thoroughly updated and revised to reflect these major developments that affect both academia and the electronics industry. Readers familiar with the First Edition will find much new material, including: * Latest U.S. and international regulatory requirements * PSpice used throughout the textbook to simulate EMC analysis solutions * Methods of designing for Signal Integrity * Fortran programs for the simulation of Crosstalk supplied on a CD * OrCAD(r) PSpice(r) Release 10.0 and Version 8 Demo Edition software supplied on a CD * The final chapter on System Design for EMC completely rewritten * The chapter on Crosstalk rewritten to simplify the mathematics Detailed, worked-out examples are now included throughout the text. In addition, review exercises are now included following the discussion of each important topic to help readers assess their grasp of the material. Several appendices are new to this edition including Phasor Analysis of Electric Circuits, The Electromagnetic Field Equations and Waves, Computer Codes for Calculating the Per-Unit-Length Parameters and Crosstalk of Multiconductor Transmission Lines, and a SPICE (PSPICE) tutorial. Now thoroughly updated, the Second Edition of Introduction to Electromagnetic Compatibility remains the textbook of choice for university/college EMC courses as well as a reference for EMC design engineers. An Instructor's Manual presenting detailed solutions to all the problems in the book is available from the Wiley editorial department.

The Processing Tax John Wiley & Sons

When designing an electronic circuit it is necessary to take a number of precautions to ensure that its EMC performance requirements can be met. Trying to fix the EMC performance once the circuit has been designed and built will be far more difficult and costly. There are a number of areas that can be addressed during the circuit design and PCB layout stage to ensure that the EMC performance is optimized: -PCB Circuit design -PCB Circuit partitioning-PCB Grounding-PCB Routing-EMC Filters-I/O Filtering and ShieldingBy adopting these precautions, the EMC performance of PCB layout can be greatly enhanced

High-Speed Digital System Design Wiley-Interscience

This practical resource provides an overview of machine learning (ML) approaches as applied to electromagnetics and antenna array processing. Detailed coverage of the main trends in ML, including uniform and random array processing (beamforming and detection of angle of arrival), antenna optimization, wave propagation, remote sensing, radar, and other aspects of electromagnetic design are explored. An introduction to machine learning principles and the most common machine learning architectures and algorithms used today in electromagnetics and other applications is presented, including basic neural networks, gaussian processes, support vector machines, kernel methods, deep learning, convolutional neural networks, and generative adversarial networks. Applications in electromagnetics and antenna array processing that are solved using machine learning are discussed, including antennas, remote sensing, and target classification.

Electromagnetic Compatibility Pocket Guide IET

Analog Circuit Design is based on the yearly Advances in Analog Circuit Design workshop. The aim of the workshop is to bring together designers of advanced analogue and RF circuits for the purpose of studying and discussing new possibilities and future developments in this field. Selected topics for AACD 2007 were: (1) Sensors, Actuators and Power Drivers for the Automotive and Industrial Environment; (2) Integrated PA's from Wireline to RF; (3) Very High Frequency Front Ends.