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[Official Gazette of the United States Patent and Trademark Office Springer Nature](#)

This is the first volume dedicated solely to the topic of epistemological disjunctivism. The original essays in this volume, written by leading and up-and-coming scholars on the topic, are divided into three thematic sections. The first set of chapters addresses the historical background of epistemological disjunctivism. It features essays on ancient epistemology, Immanuel Kant, J.L. Austin, Edmund Husserl, and Ludwig Wittgenstein. The second section tackles a number contemporary issues related to epistemological disjunctivism, including its relationship with perceptual disjunctivism, radical skepticism, and reasons for belief. Finally, the third group of essays extends the framework of epistemological disjunctivism to other forms of knowledge, such as testimonial knowledge, knowledge of other minds, and self-knowledge. Epistemological Disjunctivism is a timely collection that engages with an increasingly important topic in philosophy. It will appeal to researches and graduate students working in epistemology, philosophy of mind, and philosophy of perception.

[A Primer on Memory Consistency and Cache Coherence Springer Nature](#)

Modern electronics testing has a legacy of more than 40 years. The introduction of new technologies, especially nanometer technologies with 90nm or smaller geometry, has allowed the semiconductor industry to keep pace with the increased performance-capacity demands from consumers. As a result, semiconductor test costs have been growing steadily and typically amount to 40% of today's overall product cost. This book is a comprehensive guide to new VLSI Testing and Design-for-Testability techniques that will allow students, researchers, DFT practitioners, and VLSI designers to master quickly System-on-Chip Test architectures, for test debug and diagnosis of digital, memory, and analog/mixed-signal designs. Emphasizes VLSI Test principles and Design for Testability architectures, with numerous illustrations/examples. Most up-to-date coverage available, including Fault Tolerance, Low-Power Testing, Defect and Error Tolerance, Network-on-Chip (NOC) Testing, Software-Based Self-Testing, FPGA Testing, MEMS Testing, and System-In-Package (SIP) Testing, which are not yet available in any testing book. Covers the entire spectrum of VLSI testing and DFT architectures, from digital and analog, to memory circuits, and fault diagnosis and self-repair from digital to memory circuits. Discusses future nanotechnology test trends and challenges facing the nanometer design era; promising nanotechnology test techniques, including Quantum-Dots, Cellular Automata, Carbon-Nanotubes, and Hybrid Semiconductor/Nanowire/Molecular Computing. Practical problems at the end of each chapter for students.

[Climate Modeling for Scientists and Engineers Cengage Learning](#)

Many modern computer systems and most multicore chips (chip multiprocessors) support shared memory in hardware. In a shared memory system, each of the processor cores may read and write to a single shared address space. For a shared memory machine, the memory consistency model defines the architecturally visible behavior of its memory system. Consistency definitions provide rules about loads and stores (or memory reads and writes) and how they act upon memory. As part of supporting a memory consistency model, many machines also provide cache coherence protocols that ensure that multiple cached copies of data are kept up-to-date. The goal of this primer is to provide readers with a basic understanding of consistency and coherence. This understanding includes both the issues that must be solved as well as a variety of solutions. We present both highlevel concepts as well as specific, concrete examples from real-world systems. Table of Contents: Preface / Introduction to Consistency and Coherence / Coherence Basics / Memory Consistency Motivation and Sequential Consistency / Total Store Order and the x86 Memory Model / Relaxed Memory Consistency / Coherence Protocols / Snooping Coherence Protocols / Directory Coherence Protocols / Advanced Topics in Coherence / Author Biographies

[System-on-Chip Test Architectures Cengage Learning](#)

The finite-difference time-domain (FDTD) method has revolutionized antenna design and electromagnetics engineering. HereOCO's a cutting-edge book that focuses on the performance optimization and engineering applications of FDTD simulation systems. Covering the latest developments in this area, this unique resource offer you expert advice on the FDTD method, hardware platforms, and network systems. Moreover the book offers guidance in distinguishing between the many different electromagnetics software packages on the

market today. You also find a complete chapter dedicated to large multi-scale problem solving. This practical reference is supported with 250 illustrations, 128 equations, and 11 appendixes filled with helpful data processing techniques related to the FDTD method.

[Manuals Combined: U.S. Navy ELECTRONICS TECHNICIAN, VOLUMES 01 - 08 Springer Nature](#)

System-on-a-Chip (SOC) integrated circuits composed of embedded cores are now commonplace. Nevertheless, there remain several roadblocks to rapid and efficient system integration. Test development is seen as a major bottleneck in SOC design and manufacturing capabilities. Testing SOCs is especially challenging in the absence of standardized test structures, test automation tools, and test protocols. In addition, long interconnects, high density, and high-speed designs lead to new types of faults involving crosstalk and signal integrity. SOC (System-on-a-Chip) Testing for Plug and Play Test Automation is an edited work containing thirteen contributions that address various aspects of SOC testing. SOC (System-on-a-Chip) Testing for Plug and Play Test Automation is a valuable reference for researchers and students interested in various aspects of SOC testing.

[Software Development for Embedded Multi-core Systems Newnes](#)

In the last 50 years the world has been completely transformed through the use of IT. We have now reached a new inflection point. Here we present, for the first time, how in-memory computing is changing the way businesses are run. Today, enterprise data is split into separate databases for performance reasons. Analytical data resides in warehouses, synchronized periodically with transactional systems. This separation makes flexible, real-time reporting on current data impossible. Multi-core CPUs, large main memories, cloud computing and powerful mobile devices are serving as the foundation for the transition of enterprises away from this restrictive model. We describe techniques that allow analytical and transactional processing at the speed of thought and enable new ways of doing business. The book is intended for university students, IT-professionals and IT-managers, but also for senior management who wish to create new business processes by leveraging in-memory computing.

[Management Information Systems Newnes](#)

Authors Jim Jeffers and James Reinders spent two years helping educate customers about the prototype and pre-production hardware before Intel introduced the first Intel Xeon Phi coprocessor. They have distilled their own experiences coupled with insights from many expert customers, Intel Field Engineers, Application Engineers and Technical Consulting Engineers, to create this authoritative first book on the essentials of programming for this new architecture and these new products. This book is useful even before you ever touch a system with an Intel Xeon Phi coprocessor. To ensure that your applications run at maximum efficiency, the authors emphasize key techniques for programming any modern parallel computing system whether based on Intel Xeon processors, Intel Xeon Phi coprocessors, or other high performance microprocessors. Applying these techniques will generally increase your program performance on any system, and better prepare you for Intel Xeon Phi coprocessors and the Intel MIC architecture. A practical guide to the essentials of the Intel Xeon Phi coprocessor Presents best practices for portable, high-performance computing and a familiar and proven threaded, scalar-vector programming model Includes simple but informative code examples that explain the unique aspects of this new highly parallel and high performance computational product Covers wide vectors, many cores, many threads and high bandwidth cache/memory architecture

Multicore DSP Artech House
Over 1,600 total pages ... 14097 FIRE CONTROLMAN SUPERVISOR Covers Fire Controlman supervisor responsibilities, organization, administration, inspections, and maintenance; supervision and training; combat systems, subsystems, and their maintenance; and weapons exercises. 14098 FIRE CONTROLMAN, VOLUME 01, ADMINISTRATION AND SAFETY Covers general administration, technical administration, electronics safety, and hazardous materials as they pertain to the FC rating. 14099A FIRE CONTROLMAN, VOLUME 02--FIRE CONTROL SYSTEMS AND RADAR FUNDAMENTALS Covers basic radar systems, fire control systems, and radar safety as they relate to the Fire Controlman rating. 14100 FIRE CONTROLMAN, VOLUME 03--DIGITAL DATA SYSTEMS Covers computer and peripheral fundamentals and operations, configurations and hardware, operator controls and controlling units, components and circuits, central processing units and buses, memories, input/output and interfacing, instructions and man/machine interfaces, magnetic tape storage, magnetic disk storage, CD-ROM storage, printers, data conversion devices, and switchboards. 14101 FIRE CONTROLMAN, VOLUME 04--FIRE CONTROL MAINTENANCE CONCEPTS Introduces the Planned Maintenance System and discusses methods for identifying and isolating system faults, liquid cooling systems used by Fire Controlmen, battery alignment (purpose, equipment, and alignment considerations), and radar collimation. 14102 FIRE CONTROLMAN, VOLUME 05--DISPLAY SYSTEMS AND DEVICES Covers basic display devices and input devices associated with Navy tactical data systems as used by the FC rating. 14103 FIRE CONTROLMAN, VOLUME 06--DIGITAL COMMUNICATIONS Covers the fundamentals of data communications, the Link-11 and Link-4A systems, and local area networks. 14104A FIREMAN Provides information on the following subject areas: engineering administration; engineering fundamentals; the basic steam cycle; gas turbines; internal combustion engines; ship propulsion; pumps, valves, and piping; auxiliary machinery and equipment; instruments; shipboard electrical equipment; and environmental controls.

[Proceedings of the Future Technologies Conference \(FTC\) 2020, Volume 2 Morgan Kaufmann](#)

The only book to offer special coverage of the fundamentals of multicore DSP for implementation on the TMS320C66xx SoC This unique book provides readers with an understanding of the TMS320C66xx SoC as well as its constraints. It offers critical analysis of each element, which not only broadens their knowledge of the subject, but aids them in gaining a better understanding of how these elements work so well together. Written by Texas Instruments' First DSP Educator Award winner, Naim Dahnoun, the book teaches readers how to use the development tools, take advantage of the maximum performance and functionality of this processor and have an understanding of the rich content which spans from architecture, development tools and programming models, such as OpenCL and OpenMP, to debugging tools. It

also covers various multicore audio and image applications in detail. Additionally, this one-of-a-kind book is supplemented with: A rich set of tested laboratory exercises and solutions Audio and Image processing applications source code for the Code Composer Studio (integrated development environment from Texas Instruments) Multiple tables and illustrations With no other book on the market offering any coverage at all on the subject and its rich content with twenty chapters, Multicore DSP: From Algorithms to Real-time Implementation on the TMS320C66x SoC is a rare and much-needed source of information for undergraduates and postgraduates in the field that allows them to make real-time applications work in a relatively short period of time. It is also incredibly beneficial to hardware and software engineers involved in programming real-time embedded systems.

A+ Guide to Managing & Maintaining Your PC Oldfangled Publishing

Magnetic Core Memory Decoded is a detailed work describing the operation and development of a magnetic core memory system of the type originally implemented in the 1940's through to the 1960's. Core memory was a major stepping stone in the development of modern digital computer systems and is a fascinating technology which encompasses many engineering disciplines. A full explanation of the technology is provided from the basics of magnetic core flux responses through the design and implementation of circuits required to create a fully functional memory system. The system presented in this book is a non-trivial design and while it is relatively small in terms of its storage capacity it is still large enough to give the reader a full account of the technology and the amazing secrets of this technology. This book is a must for anyone interested in the history of computing or is simply interested in the development of engineering technology.

Multicore Software Development Techniques Springer Science & Business Media

The meeting Theoretical Perspectives on Autobiographical Memory was held at the Grange Hotel, Grange-over-Sands, in the Lake District region of North Western England, July 1991. The workshop was financed by a generous grant from the NATO Scientific Affairs Division under the Advanced Research Workshop programme and without this funding the meeting would not have been possible: the organisers and delegates gratefully acknowledge the support of the NATO Advanced Research Workshops programme. Thirty-five scientists from five different NATO countries attended the workshop and twenty-seven delegates presented papers. The two aims of the workshop were to bring together in one forum a number of comparatively separate approaches to autobiographical memory and to promote theory in the area generally. These aims were fulfilled in the presentations and discussions, particularly the final discussion session, in which delegates focussed on the central issues of the nature, structure, and functions of autobiographical memory and how these emerge in different research areas. The present volume contains the papers arising from the workshop. We thank Mrs. Sheila Whalley for secretarial help and Fiona Hirst and Stephen Anderson für practical assistance in coordinating registration for the workshop.

Computers in Nuclear Medicine Newnes

This step-by-step, highly visual text provides a comprehensive introduction to managing and maintaining computer hardware and software. Written by best-selling author and educator Jean Andrews, A+ GUIDE TO HARDWARE, Ninth Edition, closely integrates the CompTIA A+ Exam objectives to prepare students for the 220-901 certification exams. The new Ninth Edition also features extensive updates to reflect current technology, techniques, and industry standards in the dynamic, fast-paced field of PC repair and information technology. Each chapter covers both core concepts and advanced topics, organizing material to facilitate practical application and encourage students to learn by doing. The new edition features more coverage of updated hardware, security, and increased emphasis on mobile devices. Supported by a wide range of supplemental resources to enhance learning with Lab Manuals, CourseNotes, online labs and the optional MindTap that includes labs, certification test prep and interactive exercises and activities, this proven text offers students an ideal way to prepare for success as a professional IT support technician. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

Organizational, Direct Support, General Support and Depot Manual Chronicle Books

This step-by-step, highly visual text provides a comprehensive introduction to managing and maintaining computer hardware and software. Written by best-selling author and educator Jean Andrews, A+ GUIDE TO MANAGING AND MAINTAINING YOUR PC closely integrates the CompTIA A+ Exam objectives to prepare you for the 220-801 and 220-802 certification exams. The new Eighth Edition also features extensive updates to reflect current technology, techniques, and industry standards in the dynamic, fast-paced field of PC repair. Each chapter covers both core concepts and advanced topics, organizing material to facilitate practical application and encourage you to learn by doing. Supported by a wide range of supplemental resources to enhance learning—including innovative tools, interactive exercises and activities, and online study guides—this proven text offers an ideal way to prepare you for success as a professional PC repair technician. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

Core Memory SIAM

Over 1,300 total pages 14086A Electronics Technician, Volume 1 Safety and Administration "This is the first volume in the ET Training Series. Covers causes and prevention of mishaps, handling of hazardous materials; identifies the effects of electrical shock; purpose of the tag-out bill and personnel responsibilities, documents, and procedures associated with tag out; and identifies primary safety equipment associated with ET work. Provides an overview of general and technical administration and logistics. Included are descriptions of forms and procedures included in the Maintenance Data System (MDS) and publications that should be included in a ship's technical library. Also included is a basic description of the Naval Supply System and COSAL. This volume combines the previous ET volumes 1 & 2 and has been updated. 14087 ELECTRONICS TECHNICIAN, VOLUME 02--ADMINISTRATION OBSOLETE: no further enrollments allowed. Provides an overview of general and technical administration and logistics. Included are descriptions of forms and procedures included in the Maintenance Data System (MDS) and publications that should be included in a ship's technical library. Also included is a basic description of the Naval Supply System and COSAL. 14088 ELECTRONICS TECHNICIAN, VOLUME 03--COMMUNICATIONS SYSTEMS Provides operations-related information on Navy communications systems including SAS, TEMPEST, satellite communications, Links 11, 4-A, and 16, the C2P system, and a basic introduction to local area networks (LANs). 14089 ELECTRONICS TECHNICIAN, VOLUME 04--RADAR SYSTEMS Provides a basic introduction to air search, surface search, ground-controlled approach, and carrier controlled approach RADAR systems. Included are basic terms associated with RADAR systems, descriptions of equipment that compose the common systems, descriptions of RADAR interfacing procedures and equipment, and primary radar safety topics. 14090 ELECTRONICS TECHNICIAN, VOLUME 05--NAVIGATION SYSTEMS Introduces the primary navigation systems used by U.S. Navy surface vessels. It provides a basic introduction to and explanation of the Ship's Inertial Navigation System (SINS), the U.S. Navy Navigation Satellite System (NNSS), and the NAVSTAR Global Positioning System (GPS) and associated equipment. It then provides an introduction to and explanation of the Tactical Air Navigation system (TACAN) and its associated equipment. The information provided is written at an introductory level and is not intended to be used by technicians for diagnoses or repairs. 14091 ELECTRONICS TECHNICIAN, VOLUME 06--DIGITAL DATA SYSTEMS Covers the following subject matter on computers and peripherals: fundamentals and operations, configurations and hardware, operator controls and controlling units, components and circuits, central processing units and buses, memories, input/output and interfacing, instructions and man/machine interfaces, magnetic

tape storage, magnetic disk storage, CD-ROM storage, printers, data conversion devices and switchboards. 14092 ELECTRONICS TECHNICIAN, VOLUME 07--ANTENNAS AND WAVE PROPAGATION Covers a basic introduction to antennas and wave propagation. It includes discussions about the effects of the atmosphere on rf communications, the various types of communications and radar antennas in use today, and a basic discussion of transmission lines and waveguide theory. 14093 ELECTRONICS TECHNICIAN, VOLUME 08--SUPPORT SYSTEMS Provides a basic introduction to support systems: liquid cooling, dry air, ac power distribution, ship's input, and information transfer. It includes discussions on configuration, operation and maintenance of these systems.

Theoretical Perspectives on Autobiographical Memory Springer Science & Business Media

A stunning array of full-color photographs captures the history of modern technology through images of the computer collection of the Computer History Museum in Silicon Valley, offering revealing glimpses of such seminal machines as the Eniac, Crays 1-3, and Apple I and II, while describing each model, their innovations, and place in computer history.

Manuals Combined: U.S. Navy FIRE CONTROLMAN Volumes 01 - 06 & FIREMAN Springer Nature

This book provides a structured introduction of the key concepts and techniques that enable in-/near-memory computing. For decades, processing-in-memory or near-memory computing has been attracting growing interest due to its potential to break the memory wall. Near-memory computing moves compute logic near the memory, and thereby reduces data movement. Recent work has also shown that certain memories can morph themselves into compute units by exploiting the physical properties of the memory cells, enabling in-situ computing in the memory array. While in- and near-memory computing can circumvent overheads related to data movement, it comes at the cost of restricted flexibility of data representation and computation, design challenges of compute capable memories, and difficulty in system and software integration. Therefore, wide deployment of in-/near-memory computing cannot be accomplished without techniques that enable efficient mapping of data-intensive applications to such devices, without sacrificing accuracy or increasing hardware costs excessively. This book describes various memory substrates amenable to in- and near-memory computing, architectural approaches for designing efficient and reliable computing devices, and opportunities for in-/near-memory acceleration of different classes of applications.

Computers and Data Processing Systems Jeffrey Frank Jones

Kai H. Lee, PhD This book helps you acquire a basic understanding of how computers work and the processing techniques used to obtain diagnostic information for radionuclide images. The easy-to-use workbook format makes this a great educational tool.

Technology Assessment & Forecast Springer Science & Business Media

This book describes the recent innovation of deep in-memory architectures for realizing AI systems that operate at the edge of energy-latency-accuracy trade-offs. From first principles to lab prototypes, this book provides a comprehensive view of this emerging topic for both the practicing engineer in industry and the researcher in academia. The book is a journey into the exciting world of AI systems in hardware.

Magnetic Core Memory Decoded Routledge

Recent achievements in hardware and software development, such as multi-core CPUs and DRAM capacities of multiple terabytes per server, enabled the introduction of a revolutionary technology: in-memory data management. This technology supports the flexible and extremely fast analysis of massive amounts of enterprise data. Professor Hasso Plattner and his research group at the Hasso Plattner Institute in Potsdam, Germany, have been investigating and teaching the corresponding concepts and their adoption in the software industry for years. This book is based on the first online course on the openHPI e-learning platform, which was launched in autumn 2012 with more than 13,000 learners. The book is designed for students of computer science, software engineering, and IT related subjects. However, it addresses business experts, decision makers, software developers, technology experts, and IT analysts alike. Plattner and his group focus on exploring the inner mechanics of a column-oriented dictionary-encoded in-memory database. Covered topics include - amongst others - physical data storage and access, basic database operators, compression mechanisms, and parallel join algorithms. Beyond that, implications for future enterprise applications and their development are discussed. Readers are lead to understand the radical differences and advantages of the new technology over traditional row-oriented disk-based databases.

NBS Special Publication Society of Nuclear Medicine, Incorporated

This book provides the state-of-the-art intelligent methods and techniques for solving real-world problems along with a vision of the future research. The fifth 2020 Future Technologies Conference was organized virtually and received a total of 590 submissions from academic pioneering researchers, scientists, industrial engineers, and students from all over the world. The submitted papers covered a wide range of important topics including but not limited to computing, electronics, artificial intelligence, robotics, security and communications and their applications to the real world. After a double-blind peer review process, 210 submissions (including 6 poster papers) have been selected to be included in these proceedings. One of the meaningful and valuable dimensions of this conference is the way it brings together a large group of technology geniuses in one venue to not only present breakthrough research in future technologies, but also to promote discussions and debate of relevant issues, challenges, opportunities and research findings. The authors hope that readers find the book interesting, exciting and inspiring.