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Philips' Electron Tube Manual, 1959-60

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Electronic Engineering Springer Science & Business Media

For Stirling engines to enjoy widespread application and acceptance, not only must the fundamental operation of such engines be widely understood, but the requisite analytic tools for the stimulation, design, evaluation and optimization of Stirling engine hardware must be readily available. The purpose of this design manual is to provide an introduction to Stirling cycle heat engines, to organize and identify the available Stirling engine literature, and to identify, organize, evaluate and, in so far as possible, compare non-proprietary Stirling engine design methodologies. This report was originally prepared for the National

Aeronautics and Space Administration and the U. S. Department of Energy.

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"Fetal heart rate monitoring is widely used by almost every obstetrician as a way to document the case and to help decrease health care costs. This is a short reference on the physiologic benefits, instrumentation, application and interpretation of fetal heart rate monitoring. The second half of the book uses actual FHR strips and cases to illustrate various anomalies (fetal distress, fetal distress in prematurity, fetus with CNS dysfunction). Several new drugs have been introduced for use during labor that effect FHR"--Provided by publisher. The Directory of Video, Multimedia & Audio-visual Products

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Videopac computer service manual

Rapid Prototyping of Digital Systems: Quartus II Edition provides an exciting and challenging laboratory component for undergraduate digital logic and computer design courses using FPGAs and CAD tools for simulation and hardware implementation. The more advanced topics and exercises also make this text useful for upper level courses in digital logic, programmable logic, and embedded systems. This new version of the widely used Rapid Prototyping of Digital Systems, Second Edition, now uses Altera's new Quartus II CAD tool and includes laboratory projects for Altera's UP 2 and the new UP 3 FPGA board. Rapid Prototyping of Digital Systems: Quartus II Edition includes four tutorials on the Altera Quartus II and

NIOS II tool environment, an overview of programmable logic, and IP cores with several easy-to-use input and output functions. These features were developed to help students get started quickly. Early design examples use schematic capture and IP cores developed for the Altera UP FPGA boards. VHDL is used for more complex designs after a short introduction to VHDL-based synthesis. New to this edition is an overview of System-on-a-Programmable Chip (SOPC) technology and SOPC design examples for the UP3 using Altera's new NIOS II Processor hardware and C software development tools. Moody's International Manual

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