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# Microblaze Processor Reference Guide

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my problem is with Microblaze Soft processor and Microblaze Reference Guide (MRG). I have simulated a Microblaze with a Dual-Port BRAM for instruction and for data, connected through Local Memory Bus (my project will be an hardware profiler connected to LMB).

MicroBlaze Processor Reference Guide - Columbia University  
MicroBlaze Processor Reference Guide

www.xilinx.com 5 UG984 (v2015.1) April 15, 2015  
Chapter 1 Introduction

The MicroBlaze™ Processor Reference Guide provides information about the 32-bit soft processor, MicroBlaze, which is included in the Vivado release. The document is intended as a guide to the MicroBlaze hardware architecture. Guide Contents

[MicroBlaze Processor Reference Guide](#)

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MicroBlaze Processor Reference Guide

According to the Microblaze Processor Reference Guide (UG081), hardware acceleration for the Microblaze can be implemented using either FSL or AXI-4 Streaming interfaces.

Apart from being a more common interface, is there any reason to prefer AXI-4 over FSL when designing a hardware accelerator for the Microblaze?

[MicroBlaze\(TM\) Hardware Reference Guide](#)

Microblaze Quick Reference Guide Supplement This guide describes how to install board support and example designs for Digilent Arty S7 and Arty development boards into Vivado. This will allow the instructions described in the MicroBlaze Soft Processor Quick Reference Guide to be completed.

MicroBlaze Processor

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## Reference Guide

About This Guide Welcome to the MicroBlaze Processor Reference Guide. This document provides information about the 32-bit soft processor, MicroBlaze, included in the Embedded Processor Development Kit (EDK). The document is meant as a guide to the MicroBlaze hardware and software architecture. Manual Contents

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MicroBlaze Processor Reference Guide

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Error in Microblaze Reference Guide - Community Forums open and import the hardware platform, including the MicroBlaze processor. This Quick Start Guide will walk you through creating a basic MicroBlaze™ processor system using processor preset

designs. Additional resources and information can be found on the reverse side to help you tailor a MicroBlaze processor system to your exact

### Microblaze Quick Reference Guide Supplement

[Reference ...

For this tutorial, we are going to add a Microblaze IP block using the Vivado IP Integrator tool. In addition to the Microblaze IP block, a UART ( universal asynchronous receiver/transmitter ) IP block will be added to communicate between the host PC and the soft processor core running on the Basys 3.

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MicroBlaze Processor Reference Guide www.xilinx.com UG081

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MicroBlaze Software Reference Guide - Ryerson

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MicroBlaze Processor Quick Start Guide - Xilinx

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Basys 3 Getting started in  
Microblaze [Reference.Digilentinc]

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MicroBlaze Processor Reference Guide - ENSIMAG

April 2002 www.xilinx.com  
MicroBlaze Software Reference Guide 1-800-255-7778

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Revision 10/15/01 1.9 Initial MDK (MicroBlaze Development Kit) release.

1/14/02 2.1 MDK 2.1 release  
3/02 2.2 MDK 2.2 release  
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