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Book M Morris Mano index Q. 1.20: Convert decimal +49 and +29 to binary, using the signed 2's complement representation and e Q. 1.33: The state of a 12 bit register is 100010010111. What is its content if it represents

Boolean Logic & Logic Gates: Crash Course Computer Science #3Q. 6.24: Design a counter with T flip-flops that goes through the following binary repeated sequence Q. 3.17: Draw a NAND logic diagram that implements the complement of the following function

One MUST READ book on Digital Electronics | Digital Logic and Computer Design | video in HINDI Q. 3.15: Simplify the following Boolean function F , together with the don't-care conditions d , and Q. 1.25: Represent the decimal number 5137 in (a) BCD (b) excess-3 code (c) 2421 code (d) a 6311 code Q. 3.8: Find the minterms of the following Boolean expressions by plotting each function in a map Q. 2.19: Express

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Q. 4.1: Consider the combinational circuit shown in Fig. P4.1.(a)* Derive the Boolean expressions for

L2 Introduction to combinational circuit
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Q. 3.16: Simplify the following functions, and implement them with two-level NAND gate circuits:

Q. 3.9: Find all the prime implicants for the following Boolean functions, and determine which are

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