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Xilinx® ISE® software provides an integrated flow with the ModelSim and Questa simulators, which allows you to run simulation from the Xilinx Project Navigator. The following sections cover how to install the simulator, to set the path to the simulator, and to set the simulator for your design.

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ModelSim Reference Manual, v6.5e 11 Chapter 1 Syntax and Conventions Documentation Conventions This manual uses the following conventions to define ModelSim™ command syntax. Note Neither the prompt at the beginning of a line nor the <Enter> key that ends a line is shown in the command examples. Table 1-1. Conventions for Command Syntax

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Using the ModelSim or Questa Simulator

Questa delivers a comprehensive, standards-based ABV solution, offering the choice of SystemVerilog, Property Specification Language (PSL), or both. To ease the adoption of ABV, Questa also includes the Questa Verification Library (QVL). QVL is a comprehensive SystemVerilog assertion checker and monitor library that makes it easier to adopt ABV.

Concise Manual for the Modelsim/Questasim VHDL Simulator 3 2 Projects Questasim’s mechanism to keep all source ?les of a design together is called a project. Although you can compile and simulate outside projects, it is mandatory that you make use of the project mechanism for all exercises in the System-on-Chip Designcourse.

ModelSim Reference Manual

Questa UCDB affords this ability, reducing to a single database the regression data from multiple snapshots and then querying this database for trends. Tool management.

Verification management means balancing various tools and techniques to get to closure, often with an infrastructure built on home-grown scripting and lots of manual maintenance.

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Questa VM's Testplan Tracking Tool enables us to analyze the regression failures and coverage holes more efficiently by providing the reports in multiple formats. Use of Testplan Tracking can reduce the manual effort which is required to update the verification documents at the time of closure and quicken the documentation in a more appropriate format.

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Tool Introduction. QuestaSim is part of the Questa Advanced Functional Verification Platform and is the latest tool in Mentor Graphics tool suite for Functional Verification. The tool provides simulation support for latest standards of SystemC, SystemVerilog, Verilog 2001 standard and VHDL.This tool is an advancement over Modelsim in its support for advanced Verification features like coverage ...

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ModelSim Command Reference Manual, v10.4c 13 Chapter 1 Syntax and Conventions This manual uses the following conventions to define ModelSim command syntax. Documentation Conventions The following conventions are used to define ModelSim command syntax Table 1-1. Conventions for Command Syntax Syntax notation Description [Concise Manual for the Modelsim/Questasim VHDL Simulator](#)

Questa Manual Questa combines all of these forms of stimulus generation with functional coverage to identify the functionality exercised by the automatically generated stimulus. Using functional coverage metrics(SVA or PSL) as feedback for test creation, engineers can adjust constraints to focus random testing on coverage holes.