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Part 1: Theory What is the primary difference between a kernel-level context switch between processes (address spaces) and a user-level context switch? The primary difference is that kernel-level context switches involve execution of OS code.

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Operating System Concepts – 9th Edition 8.5
Silberschatz, Galvin and Gagne ©2013 Base and Limit Registers For each process, the base and limit registers define its logical address space To protect processes from each; each process has its own memory space Base register holds the smallest legal physical memory address Limit register specifies the size of the range of accessible addresses