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 4-way set

associative =  
 1.37ns @ .83ns  
 cycle time = 2  
 cycles 8-way  
 set associative  
 = 2.03ns @  
 .79ns cycle  
 time = 3 cycles  
 Miss penalty =  
 (10/.5) = 20  
 cycles for DM  
 and 2-way;  
 10/.83 = 13  
 cycles for  
 4-way; 10/.79 =  
 13 cycles for  
 8-way.  
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 engineering, computer  
 architecture is a set of  
 rules and methods that  
 describe the  
 functionality,  
 organization, and  
 implementation of  
 computer systems.  
 Some definitions of  
 architecture define it  
 as describing the

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capabilities and programming model of a computer but not a particular implementation. In other definitions computer architecture involves instruction set architecture design, microarchitecture design, logic design, and implementation. Block diagram of a basic computer with u

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