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# Ti Ipc User Guide

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## IPC Users Guide/Tests - Texas Instruments Wiki

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### **Intro to: Inter Processor (IPC) - TI Training**

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### **SYS/BIOS Inter-Processor Communication (IPC) 1.25 (Rev. E)**

This user ' s guide is the new home of the Inter-Processor Communication (IPC) User ' s Guide (SPRUGO6). That PDF-based document will no longer be updated. This user ' s guide contains the topics in the following list. It also links to API reference documentation

for static configuration ( ) and run-time C processing ( ) for each module.

### [IPC API Documentation - software-dl.ti.com](#)

1.3 About this User Guide See the installation guide provided with IPC for installation information and instructions. • Chapter 2, "The Inter-Processor Communication Package," describes the modules in the ti.sdo.ipc package. • Chapter 3, "The Utilities Package," describes the modules in the ti.sdo.utils package. Processor SDK RTOS Software Developer ... - Texas Instruments

View TI ' s CC1310 technical documents – Errata, Application notes, User guides, Selection guides, Solution guides, White papers, Design files, More literature, Blogs

### [CC1310 Errata, Application notes, User guides ... - TI.com](#)

Additional modules in the subfolders of the ti.sdo.ipc package contain specific implementations of gates, heaps, notify drivers, transports, and various device family-specific modules. In addition, the ti.sdo.ipc package defines the following interfaces that you may implement as your own custom modules:

### [IPC Users Guide/About IPC - Texas Instruments Wiki](#)

This wiki-based user's guide is the new home of the Inter-Processor Communication (IPC) User's Guide (SPRUGO6). That PDF-based

document will no longer be updated. This user's guide contains the topics in the following list. It also links to API reference documentation for static configuration () and run-time C processing () for each module.

3.1 IPC 3.0 IPC 3.x is an evolution of the IPC product in the TI Processor SDK that abstracts the lower layer of processor fabric connection and offers a set of modules and APIs to facilitate inter-process communication. IPC 3.0 supports both Linux to SYS/BIOS and SYS/BIOS-to-SYS/BIOS communication.

[IPC Users Guide/The ti.sdo.ipc Package - Texas Instruments](#)

The IPC software package is designed to hide the lower layer hardware complexity of the multi-core devices and help users to quickly develop applications for data transfer between the cores or devices. It also provides a common API interface across all supported platforms, and maximize application software reuse.

[BIOS C6SDK 2.0 User Guide - Texas Instruments Wiki](#)

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IPC was designed with the needs of a wide variety of users in mind. In the interest of providing modules that are usable by all groups, the IPC modules are designed to limit the API actions to the basic functionality required. For example, they do not perform their own resource management.

[AM57x Processor SDK Linux ... - Texas Instruments](#)

NOTE The Ipc module is not used on Concerto F28M35x devices. Instead, the IpcMgr module (in the ti.sdo.ipc.family.f28m35x package) is used to configure the devices as described in [Using IPC on Concerto Devices](#). Concerto applications should not call any Ipc or IpcMgr APIs at runtime.

[IPC Users Guide/HeapMP Modules - Texas Instruments Wiki](#)

Manual: Develop your own protocol using device resources  
Auto: Using existing RTOS/Framework Utilities (i.e., IPC)  
Thread 1  
Thread 2  
Data CorePac 0  
CorePac 0  
Device 0  
CorePac 1  
Device 1  
What solutions exist in TI 's RTOS to perform IPC ?  
IPC – RTOS/Framework Solutions ...

[3.7. IPC — Processor SDK Linux Documentation](#)

so I studied" sprugo6e.pdf " (IPC user guide) and decided to use MessageQ. I ran " C:\ti\ipc\_1\_24\_03\_32\packages\ti\sdo\ipc\examples\multicore " and I have some questions now. Q 1: Is it possible send data from L2SRAM of one core to an other L2SRAM core via MessageQ? Q 2: How can I set the memory address of my data in MessageQ?

[IPC Users Guide/MessageQ Module - Texas Instruments Wiki](#)

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[IPC Users Guide/Rebuilding IPC - Texas Instruments Wiki](#)

// Home for ipc startup (and later config/other fxns) for rpmsg based IPC  
RTOS/TMS320C6678: IPC concept - Texas Instruments

The Texas Instruments Processor Software Development Kit (Processor-SDK) is a single scalable software platform that offers streamlined development across different TI Sitara/DSP Processors. The Processor-SDK for RTOS provides fundamental platform software and tools for development, deployment and execution of RTOS-based applications.

[IPC Users Guide/Ipc Module - Texas Instruments Wiki](#)

IPC provides an implementation of the IMessageQTransport interface called ti.sdo.ipc.transports.TransportShm (shared

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memory). You can write other implementations to meet your needs. When a transport is created via a transport-specific create() call, a remote processor ID (defined via the MultiProc module) is specified.

Semiconductor Industry Standards - Texas Instruments

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Guide/HeapMP Modules From Texas Instruments Wiki

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