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Some USRP devices are capable of being grouped to form a single, virtual device. A single uhd::usrp::multi_usrp instantiation can control such a compound of devices. Currently, the following devices support this capability: USRP2 / N2x0 Series. X3x0 Series.

USRP User 's and Developer 's Guide

Unpacking the Kit Caution To prevent electrostatic discharge (ESD) from damaging the device, ground yourself using a grounding strap or by holding a grounded object, such as your computer chassis. 1. Touch the antistatic package to a metal part of the computer chassis.

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N200/N210 Device Recovery - Ettus Knowledge Base

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USRP B205mini-i | Ettus Research, a National Instruments ... Identifying USRP Devices. Most UHD utility applications and examples have an --args parameter that takes a device address, which is expressed as a delimited string. See uhd::device_addr_t for reference. Note that the device "address" can also take configuration options. See Configuring Devices and Streamers for a list of those options.

USRP Hardware Driver and USRP Manual: Firmware and FPGA Images The USRP™ B205mini-i delivers a 1x1 SDR/cognitive radio in the size of a business card. With a wide frequency range from 70 MHz to 6 GHz and a user-programmable, industrial-grade Xilinx Spartan-6 XC6SLX150 FPGA, this flexible and compact platform is ideal for both hobbyist and OEM applications. The RF front end uses the Analog Devices AD9364 RFIC transceiver with 56 MHz of instantaneous ...

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USRP Hardware Driver and USRP Manual: USRP E3xx Series

The USRP2 is guaranteed to be functional at the time it is received by the customer. Improper use or handling of the USRP2 can easily cause the device to become non-functional. Listed below are some examples of actions which can prevent damage to the unit:

Ettus Research specializes in software defined radio (SDR) systems. The USRP platform addresses a wide range of RF applications from DC to 6 GHz.

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USRP2: The user must manually write the images onto the USRP2 SD card. USRP-N Series: The user programs an image into on-board storage, which then is automatically loaded at runtime. USRP-E Series: The host code will automatically load the FPGA at runtime.

USRP2 - Ettus Knowledge Base

mobile, tablets and Kindle, etc.

Further information on how to use Chipscope can be found in the Xilinx Chipscope Pro Software and Cores User Guide (UG029). Battery notes. The USRP E312 (and with upgraded firmware E310) supports LiIon Battery packs (e.g. AA Portable Power Corp, 749801-01). Connector. The connector J1 on E312's motherboard is a Molex 53014-6310.

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USRP User's and Devel-oper's Guide Getting Started Getting all the Software The ?rst step in using your USRP system is to get all of GNU Radio installed.

USRP-2940/2942/2943/2944/2945 Getting Started Guide ...

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USRP Hardware Driver and USRP Manual: USRP2 and N2x0 Series

N2x0 JTAG Connection Recovery Process. Download the latest FPGA images,

e.g. using: \$ uhd_images_downloader There is a sub-directory in the archive below the firmware/images called 'bit'. Use Impact to load usrp_n210_r4_fpga.bit via the programmer (the filename may be different depending on your device type and revision).. The USRP should now be able to communicate on the network (you'll see ... USRP Hardware Driver and USRP Manual: Device Identification Unbricking an N-Series Device. The first step is to flash the unit's safe-mode image, and then do a normal flash - both with the UHD Image Loader utility. Make sure you have UHD installed, and the images from before, and follow the instructions in Load the Images onto the SD card (USRP2 only).