Virtuoso Spectre Circuit Simulator User Guide

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Product Version 11.1 September 2011 - A MarketPlace of Ideas
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Virtuoso Spectre Circuit Simulator RF Analysis User Guide

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Spectre Circuit Simulator User Guide - Columbia University

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Cadence AMS Simulator User Guide - pudn.com

Spectre Circuit Simulator User Guide January 2004 3 Product Version 5.0 Preface ...

Performing Monte Carlo Analysis and Yield Analysis in RF ...

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User Guide Affirma Spectre Circuit Simulator User Guide Getting Started with Spectre To specify single components within a circuit, you must provide the following information: A unique component name for the component The names of nodes to which the component is connected The master name of the component (identi fi es the type of component)

Cadence University Program › University of Connecticut

how_do_i_simulate_transient_noise [Cad Wiki for Analog IC ...

Cadence AMS Simulator User Guide Preface September 2000 12 Product Version 1.0 Instance-Based View Switching Application Note Cadence Lbrary Manager User Guide Signalscan Waves User Guide Virtuoso Schematic Composer User Guide Verilog-AMS Language Reference Manual. Available from Open Verilog International. Verilog-XL Reference

Spectre Simulation Platform - Cadence

The Virtuoso ® Spectre ® circuit simulator is a modern circuit simulator that uses direct methods to simulate analog and digital circuits at the differential equation level. The basic capabilities of the Spectre circuit simulator are similar in function and application to SPICE, but the Spectre circuit simulator is not descended from SPICE.

Virtuoso Spectre Circuit Simulator Reference

As the industry ’ s leading solution for accurate analog simulation, the Cadence ® Spectre ® Simulation Platform contains multiple solvers to allow a designer to move easily and seamlessly between circuit-, block-, and system-level simulation tasks. The foundation of the platform is a unified set of technologies shared by all of the engines—the parser, device models, Verilog-A behavioral ...

Process Variation and Mismatch - Keysight

For more information, refer to the section on Monte Carlo Analysis in Chapter 6 of the Cadence Virtuoso Spectre Circuit Simulator User Guide, Product Version 5.1.41. The statistics Statement. The Spectre statistics control statement enables you to specify a batch-to-batch (process) and per-instance (mismatch) variations for netlist parameters.

Virtuoso Spectre Circuit Simulator User Guide

Virtuoso ® Spectre ® Circuit Simulator; Virtuoso ® UltraSim Full-chip Simulator; Virtuoso ® Spectre ® RF Simulation Option for 38500; Virtuoso ® RelXpert; Virtuoso ® Analog HSPICE Interface Option ; AMS Designer with Flexible Analog Simulation; Virtuoso ® Multi-mode Simulation with AP Simulator; Interfaces . Virtuoso ® EDIF 200 Reader; Virtuoso ...

Virtuoso Spectre Circuit Simulator and Accelerated ...

Virtuoso Spectre Circuit Simulator RF Analysis User Guide Product Version 6.2 June 2007

Spectre Accelerated Parallel Simulator

Follow the steps in circuit simulation with Spectre to simulate the circuit. Before running the simulation, go to Setup->Environment... in Virtuoso Analog Design Enviroment window, and add 'extracted' in front of 'schemetic' in Switch View List Box. After running the simulation, we will get the simulation result as the figure below.

Virtuoso Spectre Circuit Simulator User

To use Spectre's process and mismatch

model in RFDE, you need to include Spectre's process and mismatch model in a model file and add it to the model library from Virtuoso Analog Design Environment. For more information on Process and Mismatch, refer to the section on Monte Carlo Analysis in Chapter 6 of the Cadence Virtuoso Spectre Circuit Simulator User Guide , Product Version 5.1.41. Spectre User Simulation Guide Virtuoso ® Spectre ® Circuit Simulator and Accelerated Parallel Simulator User Guide Product Version 10.1.1 June 2011 HSPICE Simulation in Cadence VIrtuoso

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The Cadence ® Spectre ® Accelerated Parallel Simulator provides scalable performance and capacity—at full Spectre Circuit Simulator accuracy—for complex analog, RF, and mixed-signal blocks and subsystems with tens of thousands of devices.. The Spectre Accelerated Parallel Simulator performs advanced SPICE-accurate simulation with faster convergence, scalable performance, and higher capacity.

(For more detail on the transient noise parameters refer to the Virtuoso Spectre Circuit Simulator User Guide). noiseseed Seed for the random number generator (used by the simulator to vary the noise sources internally). Specifying the same seed allows you to reproduce a previous experiment. The default value is 1.