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VLSI DIGITAL SIGNAL PROCESSING SYSTEMS: DESIGN AND ...

He has published over 650 papers, is inventor or coinventor of 31 issued US Patents, has authored the text book VLSI Digital Signal Processing Systems: Design and Implementation (Wiley, 1999), and is the co-editor (with Takao Nishitani) of the

reference book Digital Signal Processing for Multimedia Systems (CRC Press, March 1999).

*Vlsi Digital Signal Processing Systems* VLSI Architectures - Spring 2008. Course Syllabus . Course Objectives: The course will cover the most important methodologies for designing custom or semi-custom VLSI systems for some typical signal processing applications. General techniques covered include pipelining, retiming, folding and unfolding, and systolic array design.

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VLSI Digital Signal Processing Systems: Design and ...

This "Cited by" count includes citations to the following articles in Scholar. ... VLSI digital signal processing systems: design and implementation. KK Parhi. John Wiley & Sons, 1999. ...

Ieee Transactions on Very Large Scale Integration (VLSI) Systems 12 (5), 545-549, 2004. 9780471241867: VLSI Digital Signal Processing Systems ...

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VLSI Digital Signal Processing Systems | Units of study ...

Market\_Desc: - Students in graduate level courses -

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processing. ...

Keshab K. Parhi

Chap. 2 2 VLSI Digital Signal

Processing Systems •

Textbook: – K.K. Parhi,

VLSI Digital Signal Processing

Systems: Design and

Implementation, John Wiley,

1999

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Scholar Citations

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